

Low IF Receiver Reference Design Board

National Semiconductor
RD-147
Data Conversions Applications
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1.0 Design Description

The ADC14DS105KARB is a low IF receiver subsystem reference design board that utilizes a pair of LMH6552 differential drivers and a dual ADC to enable immediate evaluation of a quadrature direct conversion or near-zero IF receiver for signal frequencies from DC to 40 MHz. This receiver architecture is commonly used in WiMAX and WCDMA receiver systems. The 1 GHz input bandwidth of the ADC and the 1.5 GHz differential amplifier gain stage enable large signal-to-noise ratio (SNR) of 73.3 decibels full scale (dBFS) and spurious free dynamic range (SFDR) greater than 85 dBFS for input signals up to 40 MHz. In addition to the LMH6552, the board includes National's ADC14DS105 dual 14-bit, 105 mega-sample per second (MSPS) low-distortion, low-noise ADC with serialized low-voltage differential signaling (LVDS) outputs; LMK02000 low-jitter clock conditioner; as well as several energy-efficient power management integrated circuits (ICs).

2.0 Features

Key Features of the ADC14DS105KARB Low IF Receiver Reference Design Board

- Enables immediate evaluation of a quadrature direct conversion or near-zero IF receiver subsystem architecture
- Demonstrates a receiver architecture commonly used in WiMAX and WCDMA receiver systems
- Configured for signal frequencies from DC to 40 MHz
- **Featured Products include :**
 - LMH6552 1.5 GHz differential current feedback amplifiers from National Semiconductor's Powerwise® family
 - ADC14DS105: 14-bit, 105 MSPS, 1 GHz input bandwidth dual channel ADC with serial LVDS outputs from National Semiconductor's Powerwise® family
 - LMK02000: low-jitter clock conditioner from National Semiconductor's Powerwise® family provides 128 fs jitter (100 Hz to 20 MHz integration bandwidth)
 - Several energy-efficient power management integrated circuits (ICs) from National Semiconductor
- Reference design performance for input signals up to 40 MHz:
 - Small-signal SNR of 74 dBFS and SFDR greater than 90 dBFS
 - Large-signal SNR of 73.3 dBFS and SFDR greater than 85 dBFS

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FIGURE 1. ADC14D105 SCHEMATIC

schematic1





FIGURE 3. ADC14D105 SCHEMATIC

schematic:3

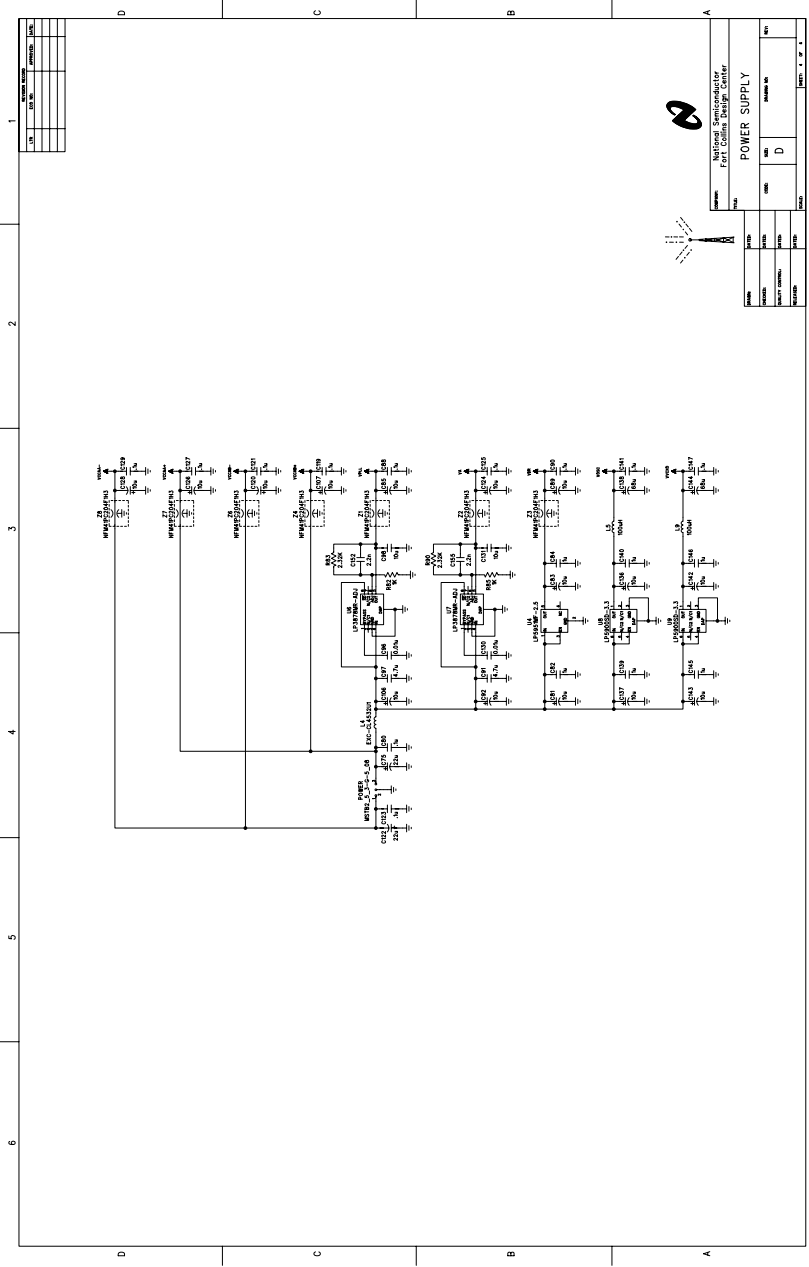


FIGURE 4. ADC14D105 SCHEMATIC

4.0 Bill of Materials

[illegible]

FIGURE 5. ADC14D105 BOM

5.0 Board Photos

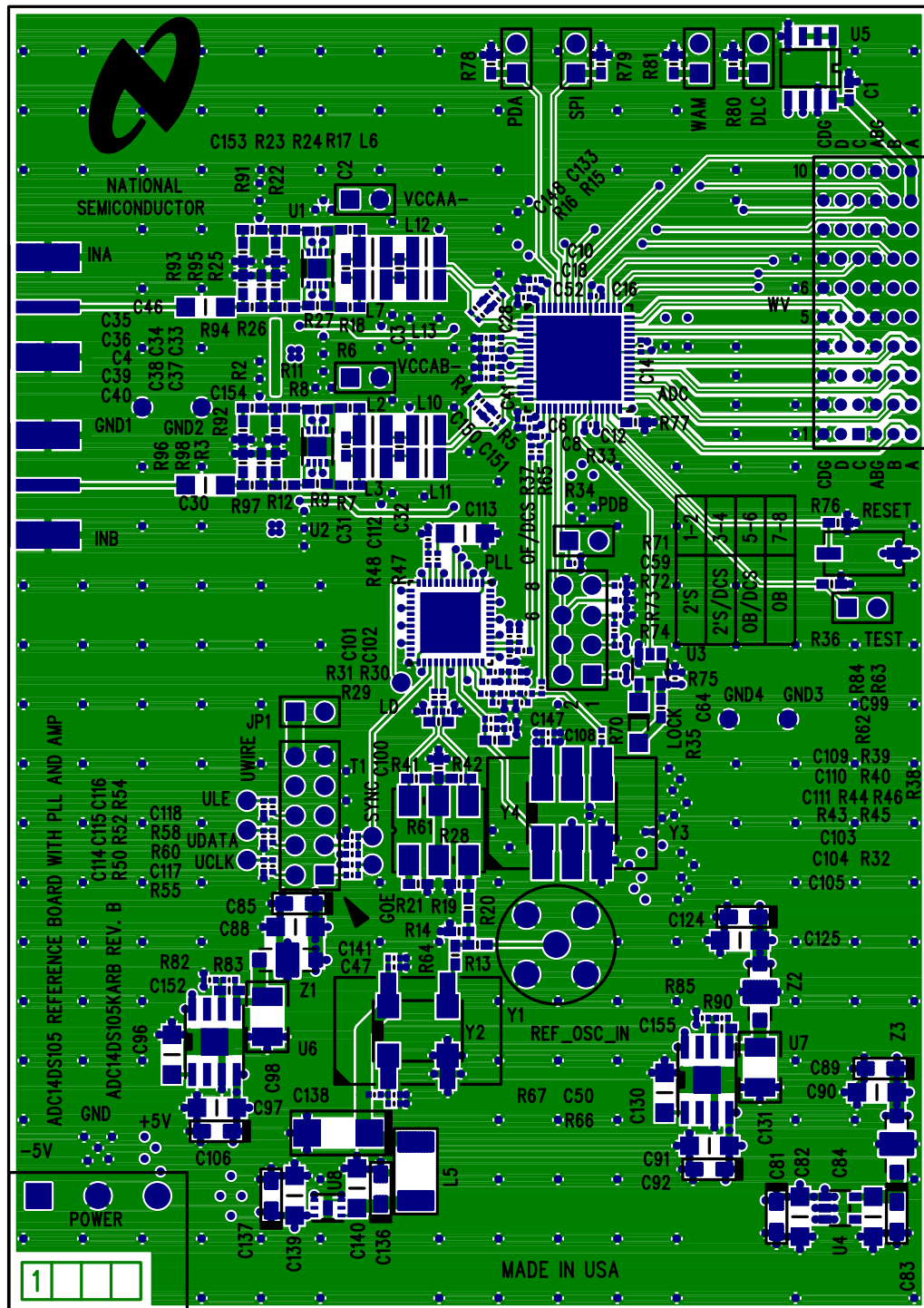


boardphoto

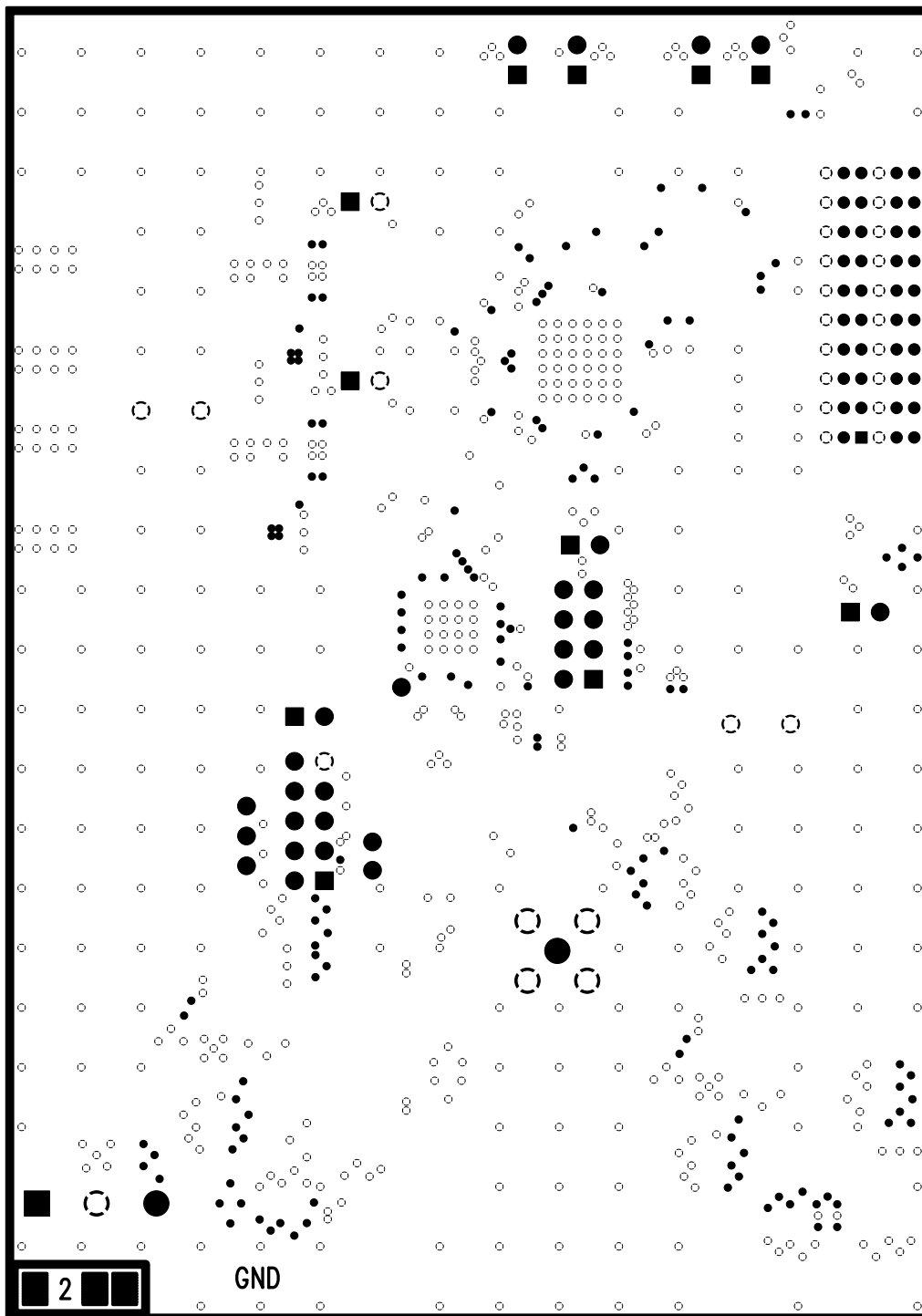
FIGURE 6. ADC14D105 Board Photo

6.0 Layouts

NATIONAL SEMICONDUCTOR LAYER1 SILK



NATIONAL SEMICONDUCTOR LAYER2



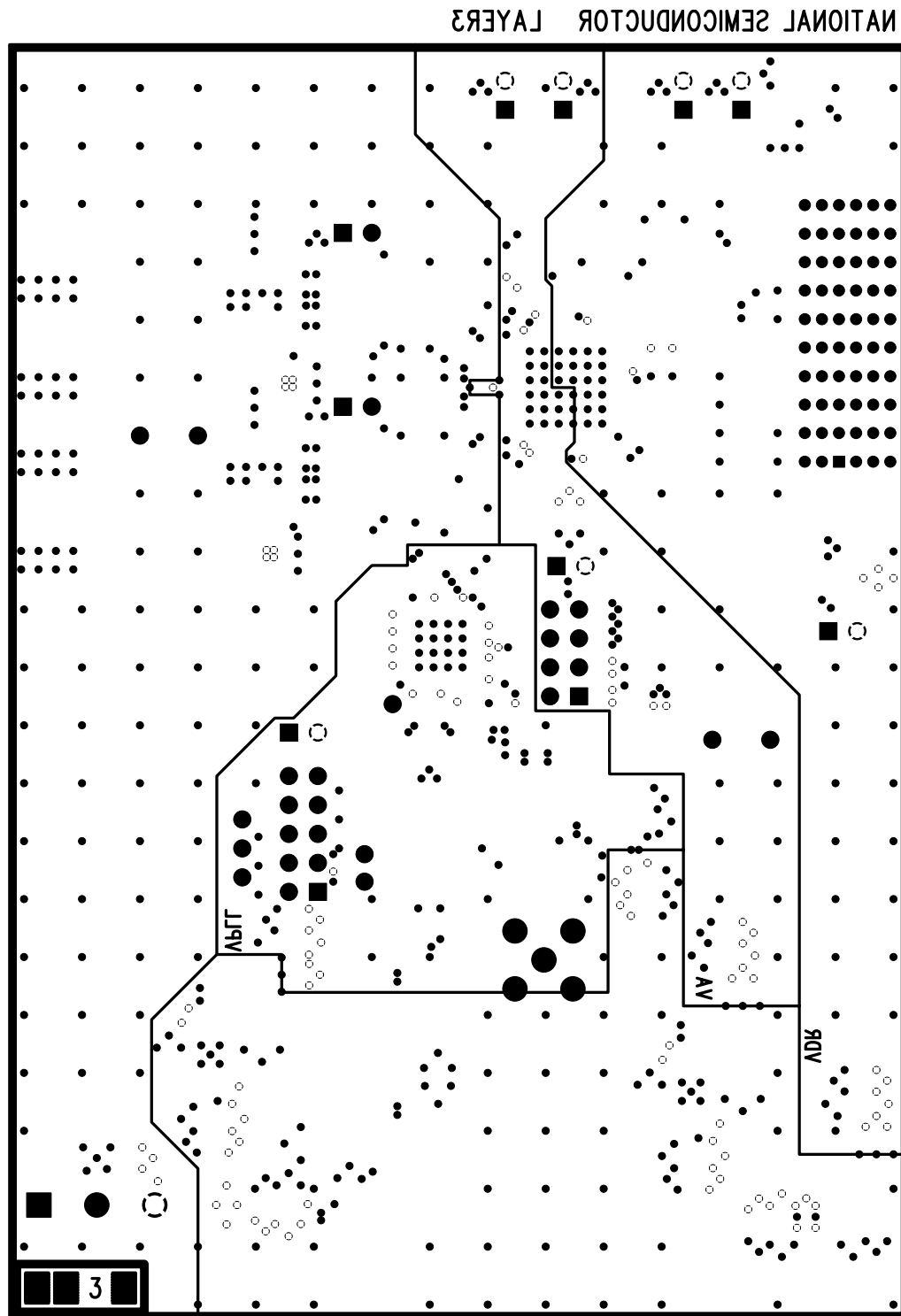


FIGURE 9. 14DS105KARBB

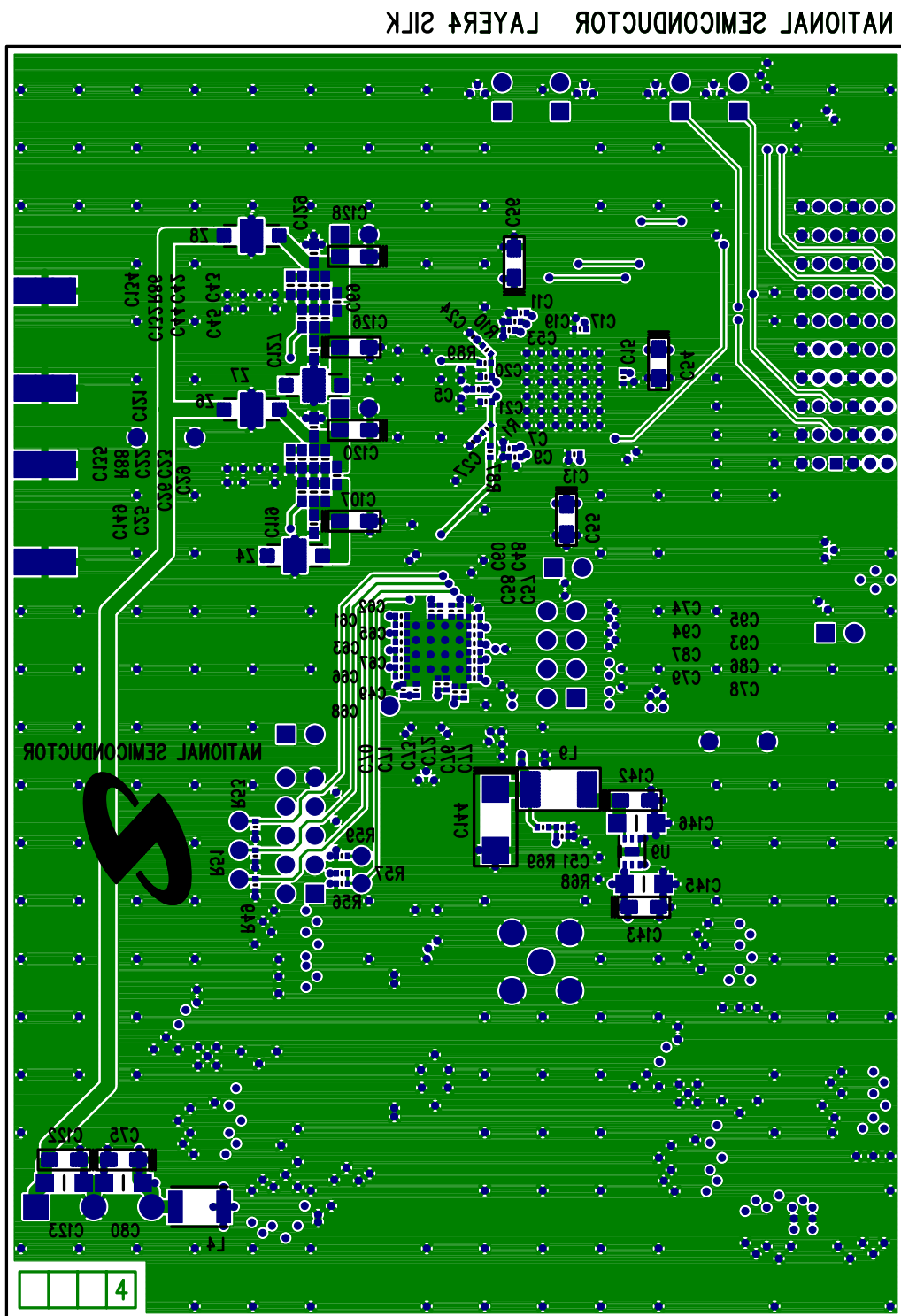


FIGURE 10. 14DS105KARBB

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